



NETL.001DV2

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jayesh R. Bhakta et al.

Group Art Unit 2827

Appl. No. : 10/674,082

Filed : September 29, 2003

For : ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE

Examiner : Ly D. Pham

AMENDMENT AND RESPONSE TO FEBRUARY 10, 2005 OFFICE ACTION

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the February 10, 2005 Office Action, Applicants request the Examiner to reconsider the above-identified patent application in view of the following amendments and remarks.

Amendments to the Claims: Begin on page 2 of this Response.

Remarks: Begin on page 5 of this Response.